NATIONAL INSTITUTE OF TECHNOLOGY, KURUKSHETRA

comp

THEORY EXAMINATION

ROLL NO._

| Date of the Examination: 19-12-19 | Programme:B.Tech |
|---------------------------------------|--------------------------------|
| Semester2nd | Subject: Digital System Design |
| Course NoITPC-10 | Maximum Marks50 |
| Number of Questions to be attempted05 | Time allowed3 Hours |
| Total No. of Questions05 | Total No. of Pages used1 |

Instructions: 1. All questions are compulsory.

2. Marks for every part of question are indicated against it.

| Q1. | (A) Convert the decimal number 30 into gray, octal, binary and excess 3 code. | 4+3+3 |
|-----|---|-------|
| | (B) Perform the following binary multiplications: (a) 1101 * 1011 (b) 0101 * 1010 (c) 100111 *011011 | |
| | (C) State and prove De Morgan's theorem. | |
| Q2. | (A)Obtain the simplified expression using SOP form $F(A,B,C,D) = \sum m (0,1,2,4,5,6,8,9,10,12,13)$ | 4+6 |
| | (B)Differentiate between:(a) Half adder and Full adder | |
| | (b)Synchronous and Asynchronous sequential circuit. | |
| Q3. | (A)What is the difference between SR flip flop and SR latch? | 2+5+3 |
| | (B)Discuss various types of shift register with suitable examples. | |
| | (C) Convert D flip flop to T flip flop. | |
| 24. | (A)What is priority encoder? Explain its working with an example. | 5+5 |
| | (B) Design BCD to Excess-3 code converter. | |
|)5. | (A)Design asynchronous BCD up counter using JK flip flop. | 5+5 |
| | (B) Differentiate between Ring and Johnson counter. | |