Reg. No.:	
Name :	



Mid-Term Examinations - October 2021					
Programme		B-Tech (BAC, BAI, BAS, BCG, BHI, BMR, BOE, BSA)	Semester	:	Fall 2021-22
Course	:	Electric Circuits and Systems	Code	:	EEE1001
Faculty	:	Dr. Abhay Vidyarthi	Slot/ Class No.	:	E11+E12+E13/0053
Time	:	1 ½ hours	Max. Marks	:	50

Answer all the Questions

Q.No.	Question Description	Marks
1	Find the current in the 2 Ω resistor in the network of Fig. 1	10
2	Find Norton's equivalent network at the terminals A and B of Fig. 2 $ \begin{array}{c} 3\Omega \\ 2\Omega \\ 6\Omega \end{array} $ Fig. 2	10
3	Determine I , V_1 , V_2 and V_0 for the series dc configuration of Fig. 3. Consider the value of 0.7v for the silicon diode to move from off state to on state. $ \begin{array}{c} + V_1 - \\ E_1 = 10 \text{ V} \\ & 4.7 \text{ k}\Omega \end{array} $ $ \begin{array}{c} V_n \\ & 4.7 \text{ k}\Omega \end{array} $ $ \begin{array}{c} V_n \\ & 4.7 \text{ k}\Omega \end{array} $	10
	Fig. 3	

4	Why the gate to source voltage is not used in an N Channel Depletion type MOSFET? What is pinch –off voltage and what happens to the drain to source current at this pinch –off voltage and what is to be done to increase the value of the drain to source current once if it reaches its saturation value? Explain the working of N Channel Depletion type MOSFET with the help of a neat sketch	10
5	Realize XOR and XNOR gates with the help of NAND and NOR gates respectively.	10